

Electronic Acknowledgement Receipt

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International Application Number:	
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Title of Invention:	Method and apparatus for manufacturing circuit board
First Named Inventor/Applicant Name:	Toshiaki Takenaka
Customer Number:	23122
Filer:	Lawrence E. Ashery
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Foreign Reference	MAT-8716US_JP2003033967. PDF	573018 3d1b27623d29223674686e28ce23a2aa646a8a3ac	no	8

Warnings:

Information:

2	NPL Documents	MAT-8716US_NPL.PDF	31689 ab7768a4903a9554fd798ff59f9798d45f1457df	no	1
Warnings:					
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3	Information Disclosure Statement Letter	MAT-8716US_IDS.PDF	64555 f58d43f382fa8da29cb2070b3541db772150c8e0	no	2
Warnings:					
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4	Information Disclosure Statement (IDS) Filed (SB/08)	MAT-8716US_IDS-1449.PDF	68917 df41ce94d6e179570751388b162d79f29c50d8d1	no	1
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